

FIG. 1

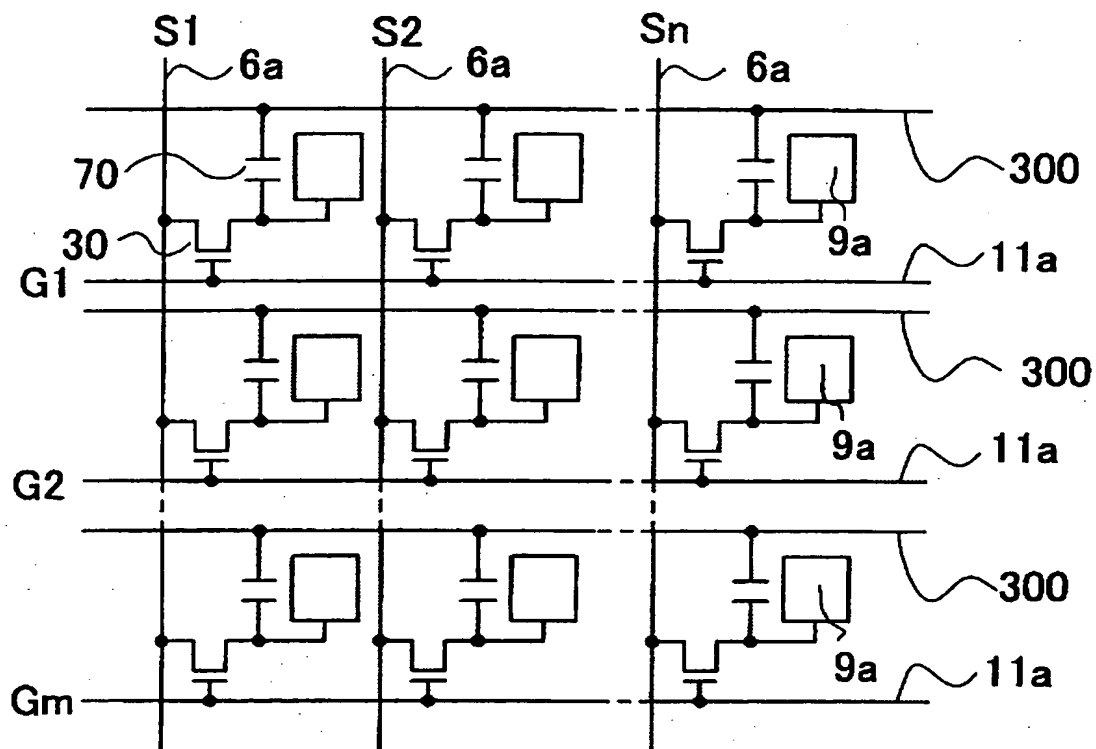


FIG. 2

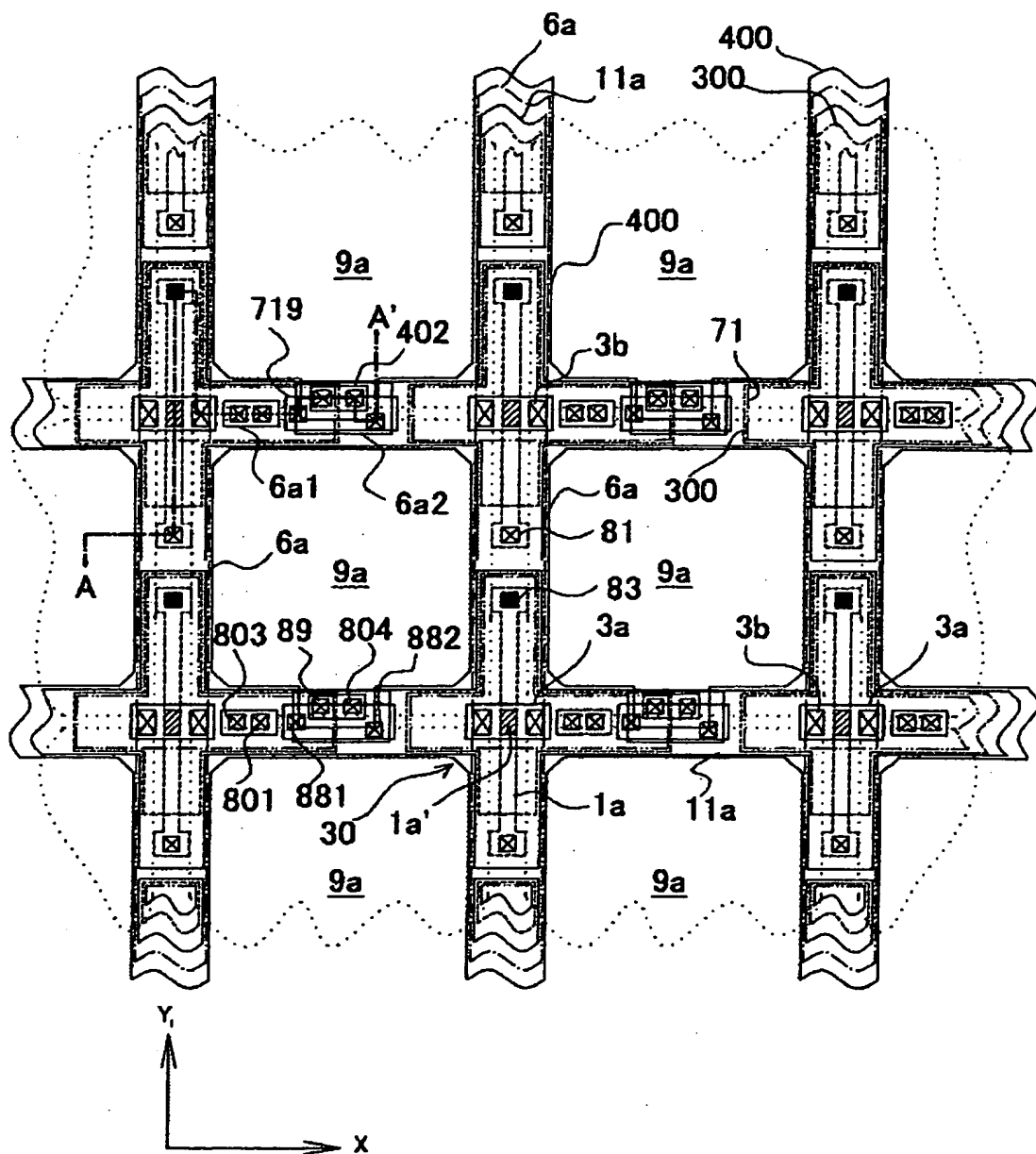


FIG. 3

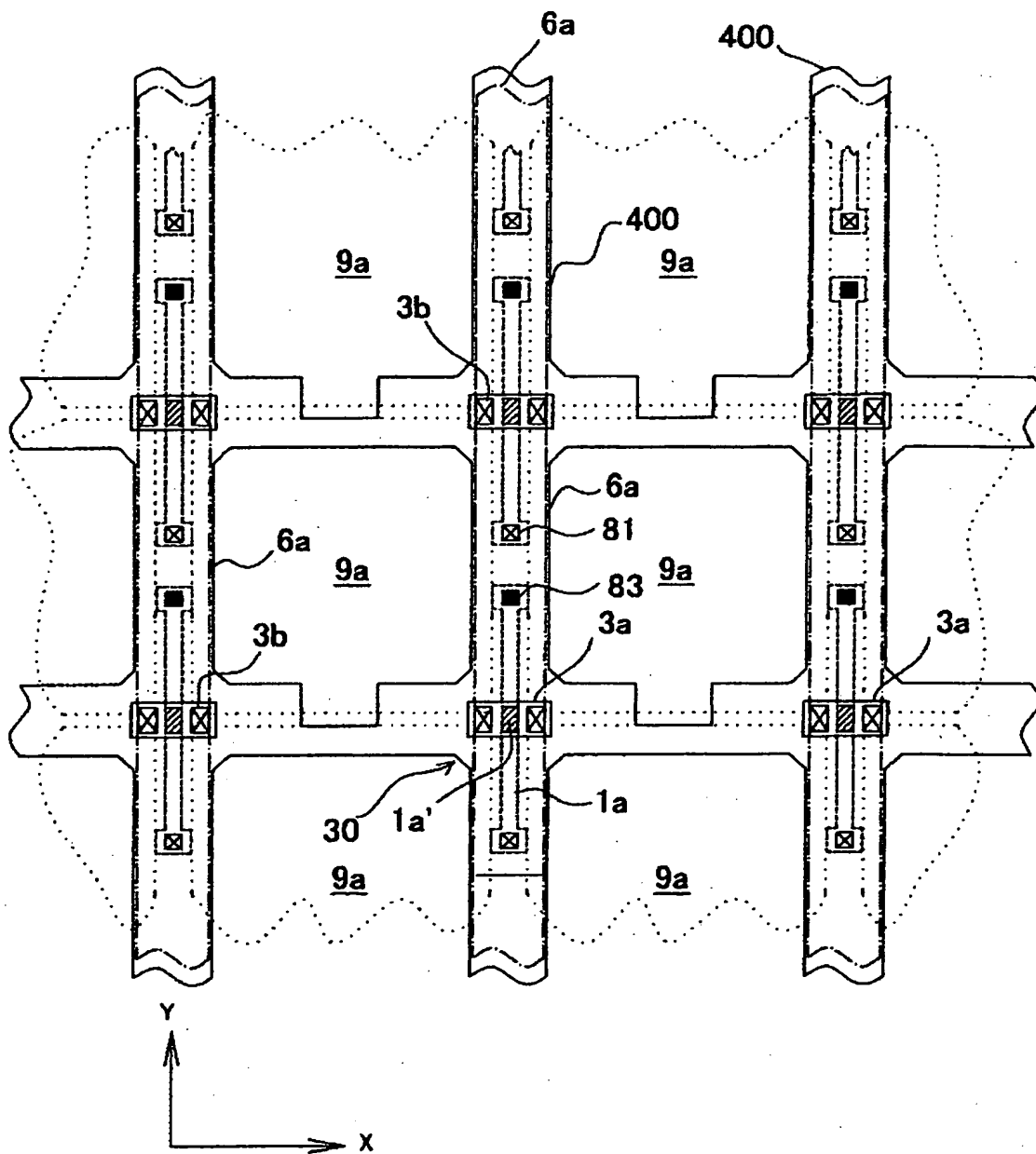


FIG. 4

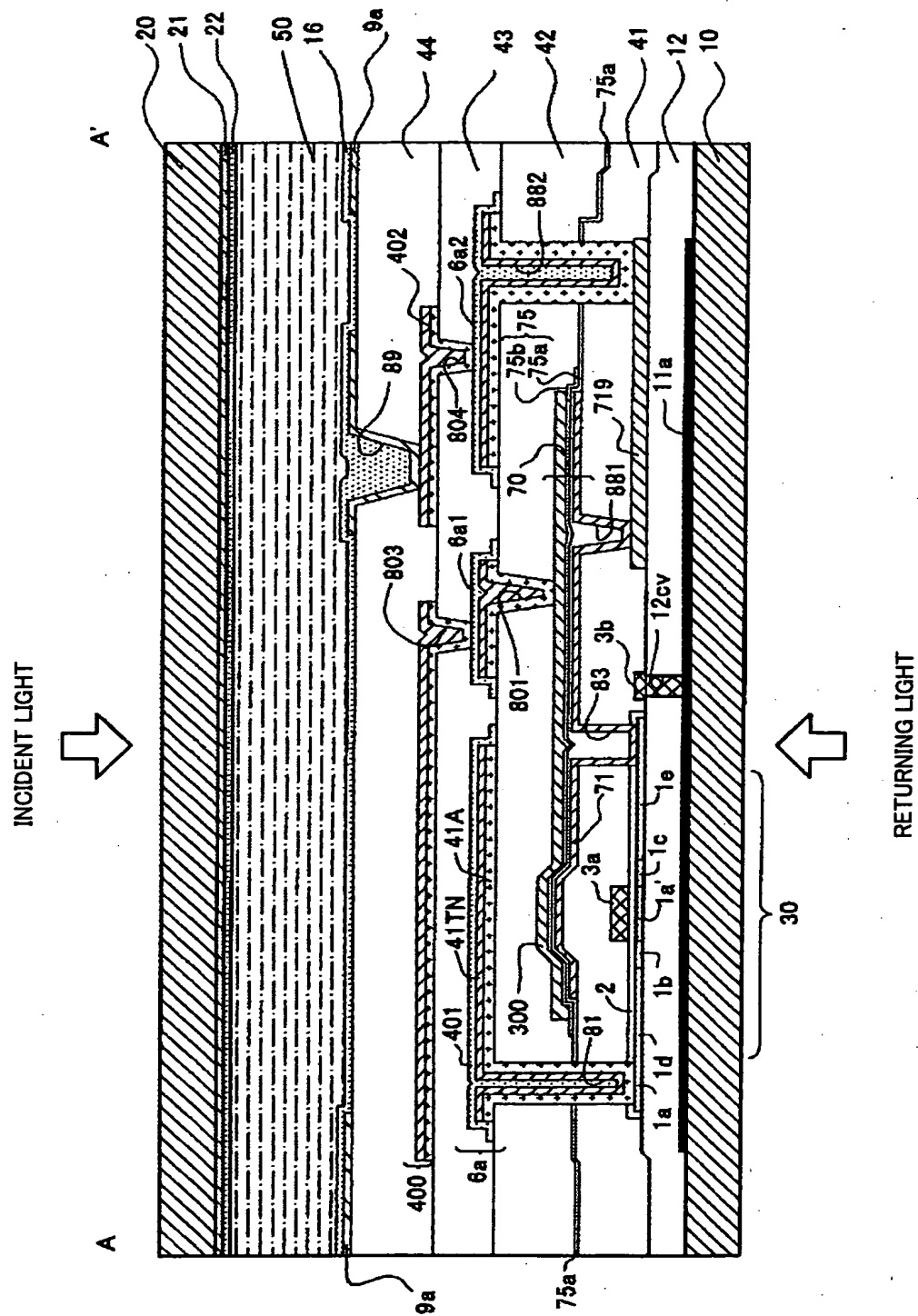
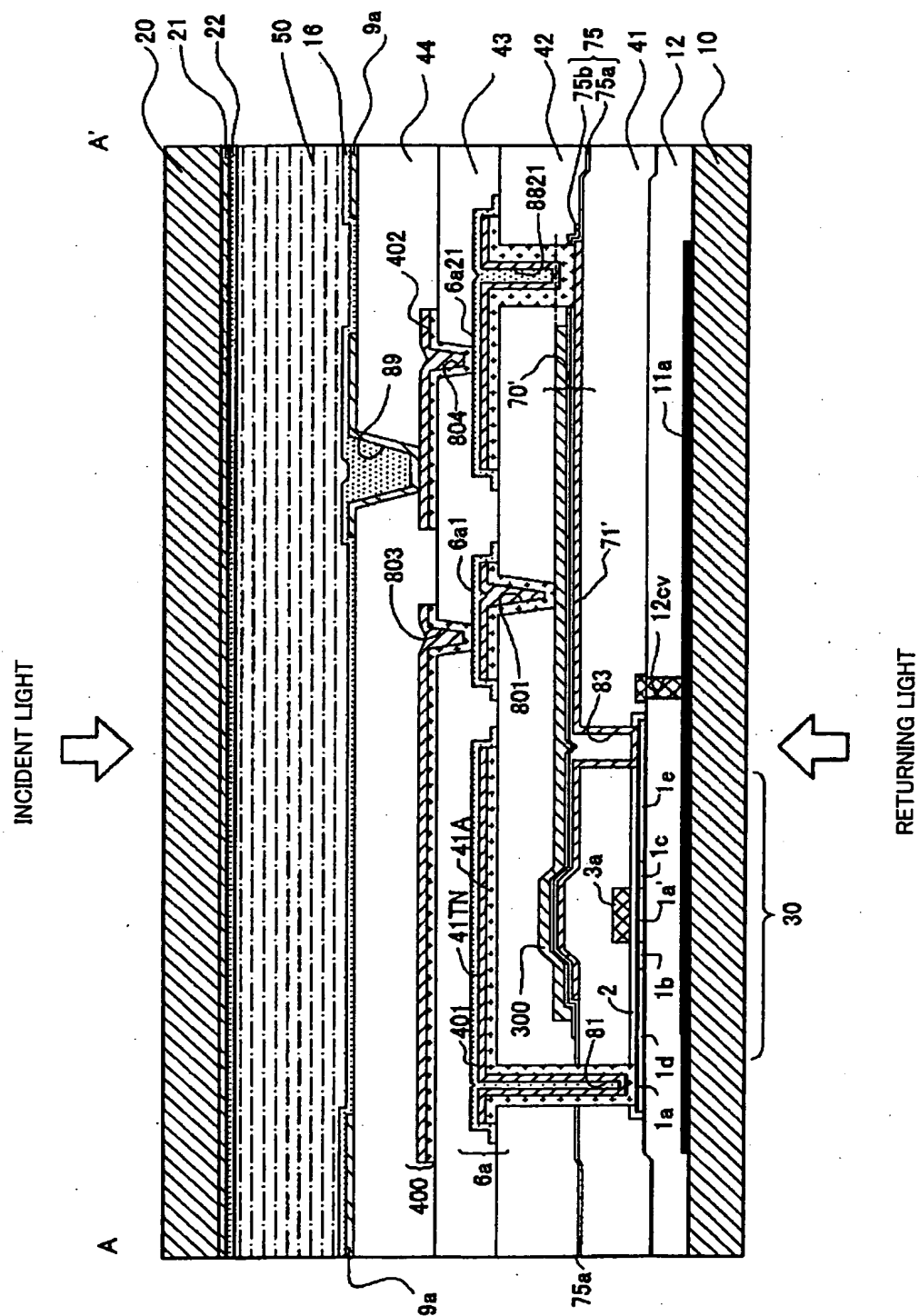


FIG. 5



This cross-sectional view shows a substrate 10 with a hatched pattern. A layer 11a is formed on the substrate. A contact layer 12cv is formed on layer 11a. A gate stack 2 is formed on the contact layer 12cv, consisting of a gate insulator 1a and a gate electrode.

[illegible]

[illegible][illegible][illegible]

FIG. 8

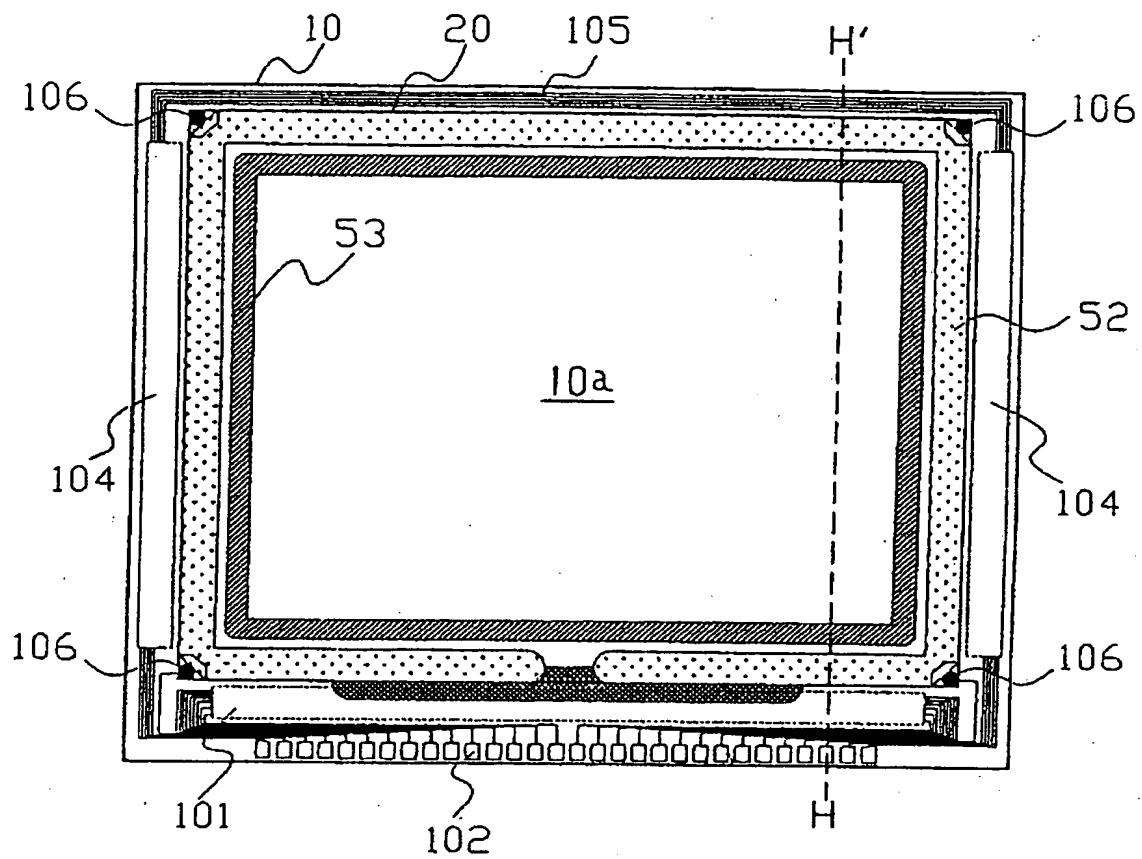


FIG. 9

